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1   **Certified email with a light on-line trusted third party: design and
❦   implementation**
    Martín Abadi, Neal Glew
    May 2002 WWW '02: Proceedings of the 11th international conference on World
            Wide Web
    Publisher: ACM
    Full text available: 📄 pdf(189.19 KB) Additional Information: full citation, abstract, cited by, index
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        This paper presents a new protocol for certified email. The protocol aims to
        combine security, scalability, easy implementation, and viable deployment.
        The protocol relies on a light on-line trusted third party; it can be
        implemented without any special ...

2   **Enabling email confidentiality through the use of opportunistic encryption**
    Simson L. Garfinkel
    May 2003 dg.o '03: Proceedings of the 2003 annual national conference on
            Digital government research
    Publisher: Digital Government Research Center
    Full text available: 📄 pdf(51.35 KB) Additional Information: full citation, abstract, references

        Software for encrypting email messages has been widely available for more
        than 15 years, but the email-using public has failed to adopt secure
        messaging. This failure can be explained through a combination of technical,
        community, and usability factors. ...

3   **Path delay fault testing using test points**
❦   S. Tragoudas, N. Denny
    January 2003 ACM Transactions on Design Automation of Electronic
            Systems (TODAES), Volume 8 Issue 1
    Publisher: ACM
    Full text available: 📄 pdf(105.98 KB) Additional Information: full citation, abstract, references,
                                                                       index terms, review

        Inserting controllable/observable points in the test architecture has been

shown to be a viable method for reducing the number of path delay faults that need to be tested in a circuit. In order to have a minimal impact on the operation clock and more ...

Keywords: Automatic test pattern generation, delay testing, design for testability, path delay fault simulation (coverage), path delay fault testing, testing digital circuits

4 A comparative study of parallel and sequential priority queue algorithms
Robert Rönngren, Rassul Ayani
April 1997 ACM Transactions on Modeling and Computer Simulation (TOMACS), Volume 7 Issue 2
Publisher: ACM

Full text available: pdf(640.10 KB)   Additional Information: full citation, abstract, references, cited by, index terms, review

Priority queues are used in many applications including real-time systems, operating systems, and simulations. Their implementation may have a profound effect on the performance of such applications. In this article, we study the performance of well-known ...

Keywords: parallel access priority queue, pending event set implementations, priority queue

5 Getting Rid of Spam: Blackmail
Brandon M. Browning
March 1998 Linux Journal, Volume 1998 Issue 47es
Publisher: Specialized Systems Consultants, Inc.
Full text available: html(13.11 KB) Additional Information: full citation, references, index terms

6 An iterative gate sizing approach with accurate delay evaluation
Guangqiu Chen, Hidetoshi Onodera, Keikichi Tamaru
December 1995 ICCAD '95: Proceedings of the 1995 IEEE/ACM international conference on Computer-aided design
Publisher: IEEE Computer Society

Additional Information: full citation,
Full text available: pdf(87.04 KB) Publisher Site   abstract, references, cited by, index terms

This paper introduces a new gate sizing approach with accurate delay evaluation. The approach solves gate sizing problems by iterating local sizing results from linear programming within small variable ranges of gate sizes. In each iterative step, variable ...

Keywords: delay evaluation, iteration, linear program, gate sizing

7 Spam, Spam, Spam, Spam, Spam, the FTC, and Spam
Eric Allman
September 2003 Queue, Volume 1 Issue 6
Publisher: ACM

Full text available: ▨ pdf(1.28 MB) ▨ html(29.58 KB) Additional Information: full citation,
abstract, cited by,
index terms

A forum sponsored by the FTC highlights just how bad spam isand and how its only going to get worse without some intervention.

The Federal Trade Commission (FTC) held a forum on spam in Washington, D.C., April 30 to May 2. Rather to my ...

8 Transistor reordering for power minimization under delay constraint
S. C. Prasad, K. Roy
April 1996 ACM Transactions on Design Automation of Electronic Systems (TODAES), Volume 1 Issue 2
Publisher: ACM

Full text available: ▨ pdf(289.98 KB) Additional Information: full citation, abstract, references, cited by, index terms

In this article we address the problem of optimization of VLSI circuits to minimize power consumption while meeting performance goals. We present a method of estimating power consumption of a basic or complex CMOS gate which takes the internal capacitances ...

Keywords: circuit optimization, critical path enumeration, gate input reordering, power estimation, transistor reordering

9 Email—the good, the bad, and the ugly
Hal Berghel
April 1997 Communications of the ACM, Volume 40 Issue 4
Publisher: ACM

Full text available: ▨ pdf(264.66 KB) Additional Information: full citation, references, cited by, index terms

10 Study of delay patterns of weighted voice traffic of end-to-end users on the VoIP network
Jeong-Soo Han, Seong-Jin Ahn, Jin-Wook Chung
August 2002 International Journal of Network Management, Volume 12 Issue 5
Publisher: John Wiley & Sons, Inc.

Full text available: ▨ pdf(290.30 KB) Additional Information: full citation, abstract, references, index terms

In this paper we study delay patterns of weighted voice traffic of end-to-end users on the Voice over Internet Protocol (VoIP) network. We compare the delay performance of voice traffic which varies with queue management techniques such as First-In First-Out ...

11 Email marketing grows up: a primer for the new millennium
Joe Dysart

December 1999 netWorker,   Volume 3 Issue 4
Publisher: ACM

Full text available: pdf(212.84 KB) html(9.32 KB)   Additional Information: full citation, index terms

12 Effects of resource sharing on circuit delay: an assignment algorithm for clock period optimization
Subhrajit Bhattacharya, Sujit Dey, Franc Breglez
April 1998 ACM Transactions on Design Automation of Electronic Systems (TODAES),   Volume 3 Issue 2
Publisher: ACM

Full text available: pdf(260.26 KB)   Additional Information: full citation, abstract, references, index terms

This paper analyzes the effect of resource sharing and assignment on the clock period of the synthesized circuit. The assignment phase assigns or binds operations of the scheduled behavioral description to a set of allocated resources. We focus on control-flow ...

Keywords: clock period, high-level synthesis, resorce sharing

13 Minimum delay optimization for domino logic circuits---a coupling-aware approach
Ki-Wook Kim, Seong-Ook Jung, Taewhan Kim, Sung-Mo Kang
April 2003 ACM Transactions on Design Automation of Electronic Systems (TODAES),   Volume 8 Issue 2
Publisher: ACM

Full text available: pdf(130.81 KB)   Additional Information: full citation, abstract, references, index terms

Minimum delay associated with the hold time requirement is of concern to circuit designers, since race-through hazards are inherent in any multiple clock organization or clock distribution tree irrespective of clock frequency. The monotonic property ...

Keywords: Logic synthesis, coupling, delay minimization, domino logic

14 Optimizing static calendar queues
K. Bruce Erickson, Richard E. Ladner, Anthony Lamarca
July 2000 ACM Transactions on Modeling and Computer Simulation (TOMACS),   Volume 10 Issue 3
Publisher: ACM

Full text available: pdf(265.99 KB)   Additional Information: full citation, abstract, references, cited by, index terms, review

The calendar queue is an important implementation of a priority queue that is particularly useful in discrete event simulators. We investigate the performance of the static calendar queue that maintains N active events. The main contribution ...

Keywords: Markov chain, algorithm analysis, calendar queue, data structures, discrete event simulation, optimization, priority queue

15 Numerical bifurcation analysis of delay differential equations using DDE-
    BIFTOOL
    K. Engelborghs, T. Luzyanina, D. Roose
    March 2002 ACM Transactions on Mathematical Software (TOMS), Volume
              28 Issue 1
    Publisher: ACM

    Full text available: pdf(366.62 KB) Additional Information: full citation, abstract, references,
                                                                 cited by, index terms

        We describe DDE-BIFTOOL, a Matlab package for numerical bifurcation
        analysis of systems of delay differential equations with several fixed, discrete
        delays. The package implements continuation of steady state solutions and
        periodic solutions and their ...

        Keywords: Continuation, delay differential equations, numerical stability and
        bifurcation analysis, software package

16 Satisfiability models and algorithms for circuit delay computation
    Luís Guerra e Silva, João Marques-Silva, L. Miguel Silveira, Karem A. Sakallah
    January 2002 ACM Transactions on Design Automation of Electronic
                Systems (TODAES), Volume 7 Issue 1
    Publisher: ACM

    Full text available: pdf(270.96 KB) Additional Information: full citation, abstract, references,
                                                                 index terms

        The existence of false paths represents a significant and computationally
        complex problem in the estimation of the true delay of combinational and
        sequential circuits. In this article we conduct a comprehensive study of
        modeling circuit delay computation, ...

        Keywords: Boolean satisfiability, circuit delay computation, delay modeling,
        false path, timing analysis

17 Adaptive proportional delay differentiated services: characterization and
    performance evaluation
    Matthew K. H. Leung, John C. S. Lui, David K. Y. Yau
    December 2001 IEEE/ACM Transactions on Networking (TON), Volume 9 Issue
                6
    Publisher: IEEE Press

    Full text available: pdf(409.36 KB) Additional Information: full citation, abstract, references,
                                                                 cited by, index terms

        We examine a proportional-delay model for Internet differentiated services.
        Under this model, an ISP can control the waiting-time "spacings" between
        different classes of traffic. Specifically, the ISP tries to ensure that the
        average waiting time of ...

        Keywords: Differentiated services, packet scheduling, proportional delay

18 Spam wars
    Lauren Weinstein
    August 2003 Communications of the ACM, Volume 46 Issue 8

Publisher: ACM

Full text available: pdf(44.64 KB) html(7.58 KB)   Additional Information: full citation, cited by, index terms

19 SPAM on the menu: the practical use of remote messaging in community care
Keith Cheverst, Karen Clarke, Dan Fitton, Mark Rouncefield, Andy Crabtree, Terry Hemmings
June 2002 ACM SIGCAPH Computers and the Physically Handicapped, Issue 73-74
Publisher: ACM

Full text available: pdf(541.44 KB)   Additional Information: full citation, abstract, references, cited by, index terms

This paper presents some early design work of the 'Digital Care' project, developing technologies to assist care in the community for user groups with different support needs. Our focus is on developing a SMS Public Asynchronous Messenger (SPAM) system ...

Keywords: SMS messaging, community care, cultural probes, ethnography, requirements, user workshops

20 Optimal integer delay budgeting on directed acyclic graphs
E. Bozorgzadeh, S. Ghiasi, A. Takahashi, M. Sarrafzadeh
June 2003 DAC '03: Proceedings of the 40th conference on Design automation
Publisher: ACM

Full text available: pdf(373.56 KB)   Additional Information: full citation, abstract, references, cited by, index terms

Delay budget is an excess delay each component of a design can tolerate under a given timing constraint. Delay budgeting has been widely exploited to improve the design quality. We present an optimal integer delay budgeting algorithm. Due to numerical ...